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TM Tirpak, I Motorola - **Simulation** Conference Proceedings, 1993. Winter, 1993 - [ieeexplore.ieee.org](#)

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Correlation of solder paste rheology with computational simulations of the stencil printing process

R Durairaj, UK Manchester, GJ Jackson, NN Ekere, G ... - **Technology**, 2002 - [emeraldinsight.com](#)

... of **solder** paste onto printed **circuit** boards (PCBs ... resulting in significant economical benefits for **manufacturing**. ... based on a Cross **model** relationship obtained ...

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Solder paste printing process modelling map

NN Ekere, SH Mannan, MA Currie - **Electronic Manufacturing Technology Symposium**, 1995, ..., 1995 - [ieeexplore.ieee.org](#)

... top-down methodology, in which any **manufacturing** process can ... A computer **simulation model** of the aperture emptying process for ... a pad of the printed **circuit** board ...

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Finite element based solder joint fatigue life predictions for a same die size-stacked-chip scale- ...

BA Zahn, W Design, CI Characterization, AZ ... - **Electronics Manufacturing Technology Symposium**, 2002. IEMT ..., 2002 - [ieeexplore.ieee.org](#)

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A Model to Explain SMT Open Circuit Joints Associated with Adjacent Solder Bridges

DC Whalley - **JOURNAL OF ELECTRONICS MANUFACTURING**, 1996 - [worldscinet.com](#)

... and the processing time which, particularly during **model** development, is ... of their potential impact on **manufacturing** yields ... to, for example, open **circuit** defects ...

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TA Nguty, NN Ekere, A Adebayo - **Electronics Manufacturing Technology Symposium**, 1999. Twenty ..., 1999 - [ieeexplore.ieee.org](#)

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was used because of its ease to ... at different regimes of the **printing** process. ...

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YY Su, K Srihari, CR Emerson - Computers & Industrial Engineering, 1997 - Elsevier

... and productivity of the **manufacturing** process ... convection oven temperature profile

simulation generated by ... Coombs, CF, (1995), Printed **Circuits** Handbook, McGraw ...

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Evaluation of printed **circuit** board assembly **manufacturing** systemsusing fuzzy colored Petri nets - *polyu.edu.hk (PDF)

SCK Shiu, ECC Tsang, DS Yeung, MB Lam - Systems, Man, and Cybernetics, 1998. 1998 IEEE International ..., 1998 - [ieeexplore.ieee.org](#)

... The constructed **model** adopted in this paper was ... **Mount** Technology process of Printed

Circuit Board Assembly ... The **manufacturing** plant is located in China and its ...

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L Li, P Thompson, M Inc, AZ Tempe - ... and Manufacturing Technology, Part C: Manufacturing, IEEE ..., 2000 - [ieeexplore.ieee.org](#)

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model prediction ... IEEE TRANSACTIONS ON ELECTRONICS PACKAGING **MANUFACTURING**, VOL ...

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Design and development of a generic PCBA line simulator

R De Souza - JOURNAL OF ELECTRONICS MANUFACTURING, 1993 - [worldscinet.com](#)

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by Charles Wick, Ramon Bakerjian, Society of ... - 1993 - Technology & Engineering - 512 pages
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A product created through the **reflow** of low melting point **solder** on select
contacts of a printed **circuit** board. In one form, the printed **circuit** board
has ...

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Is multilayer ceramic with **soldered** metal lid or glassed ceramic lid. ... characterization and **simulation** of all **circuit** elements in the interconnect chain. ...

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soak That part of the **solder reflow** process in which internal temperature or the ceramic base for a hybrid **circuit**. surface **mount** (technology) See SMT. ...

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